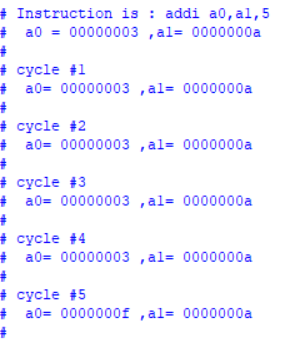
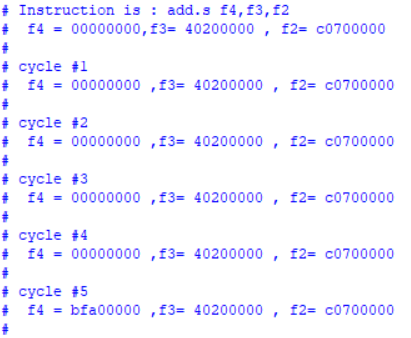
Test Bench file

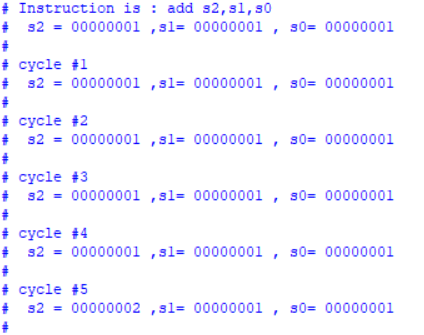
1. I-Type Addition



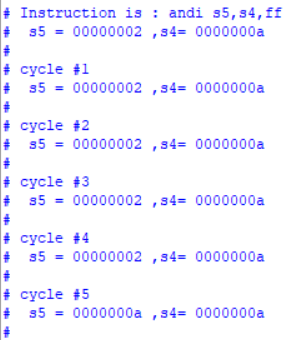
2. Floating Point Addition



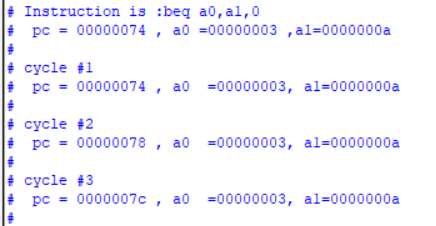
1. R-Type Addition



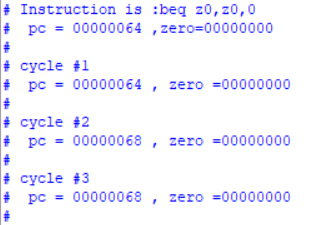
4. I-Type And



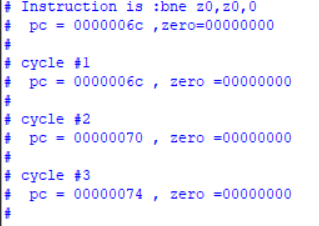
5. Branch Equal (Not Taken)



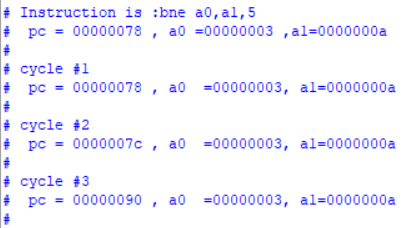
6. Branch Equal (Taken)



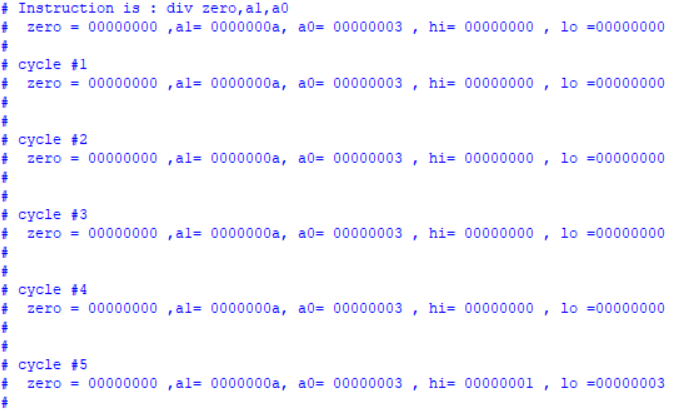
7. Branch Not Equal (Not Taken)



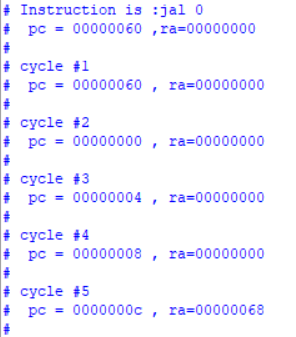
8. Branch Equal (Taken)



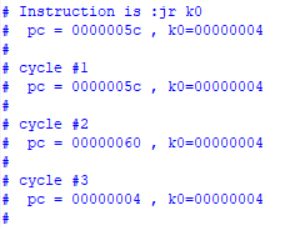
9. Division



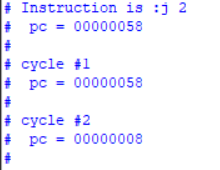
10.Jump And Link



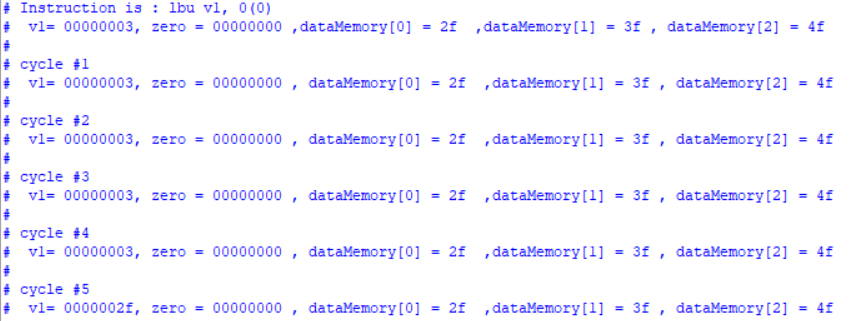
11.Jump Register



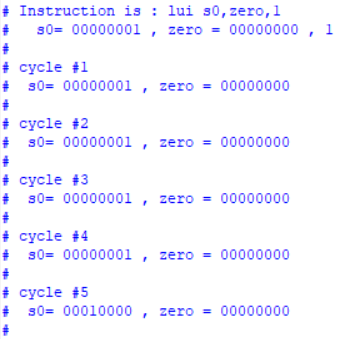
12.Jump

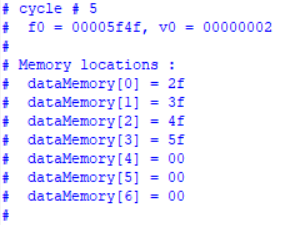
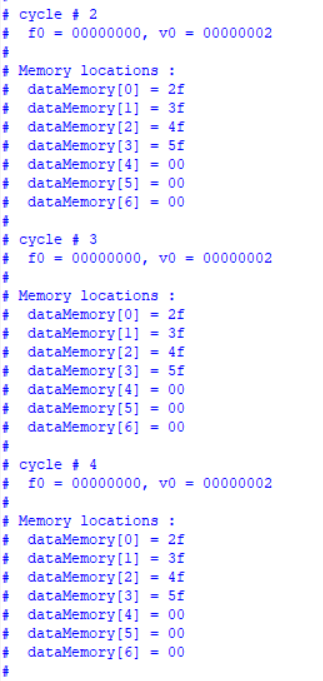
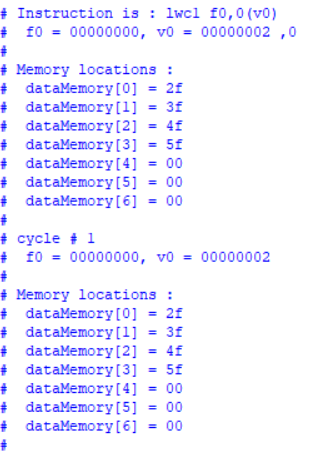


13.Load Byte Unsigned

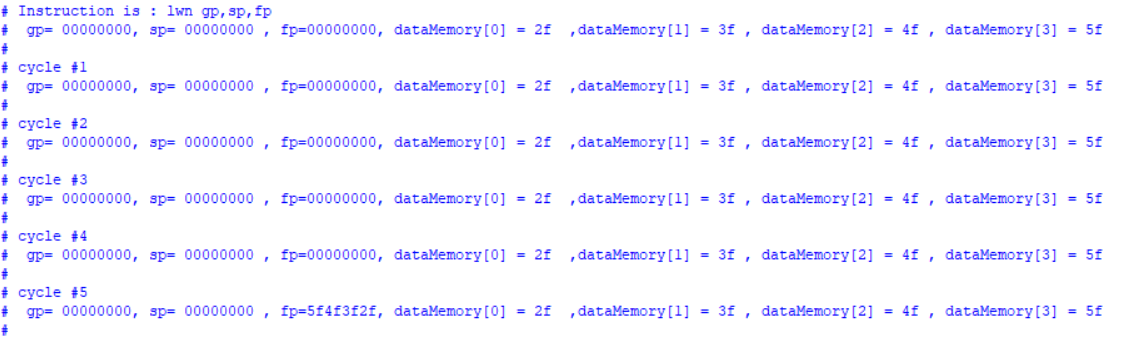


14.Load Upper Immediate

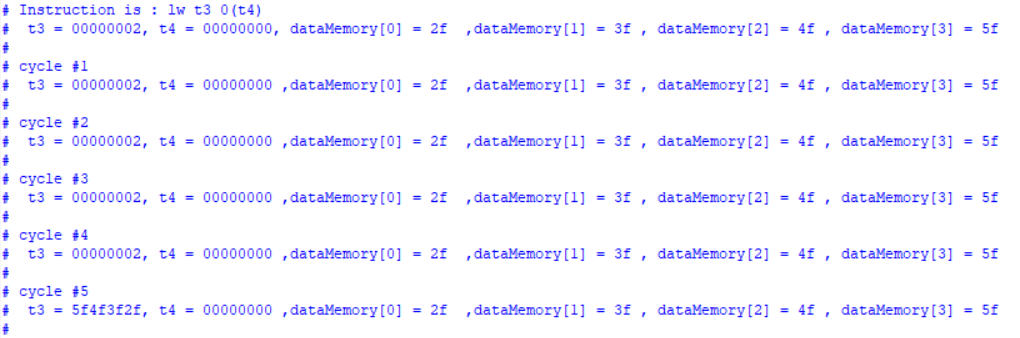


15.Load Floating Point  


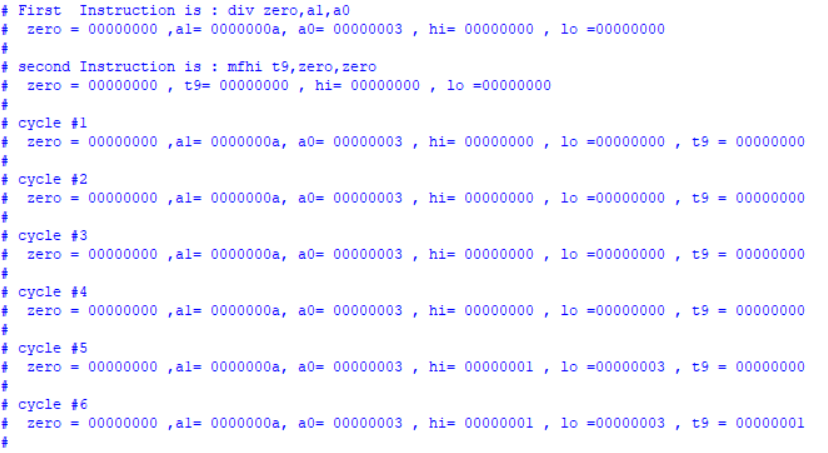
16.Load Word New



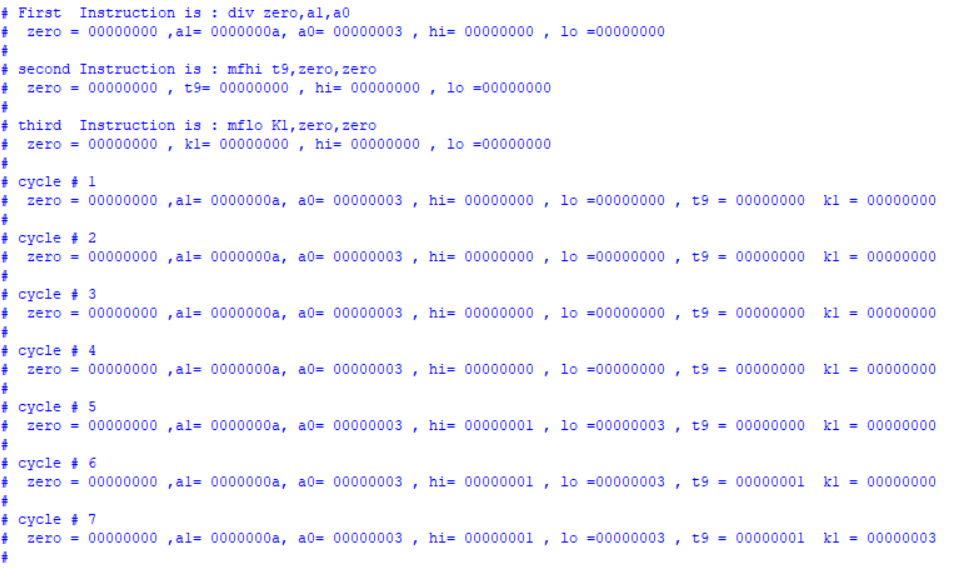
17.Load Word



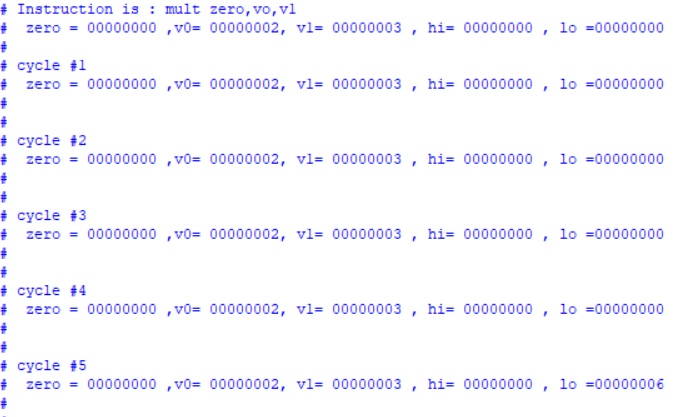
18.Move From High After Division



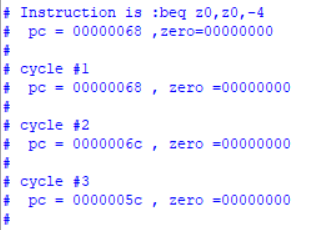
19. Move From Low After Move From High After Division



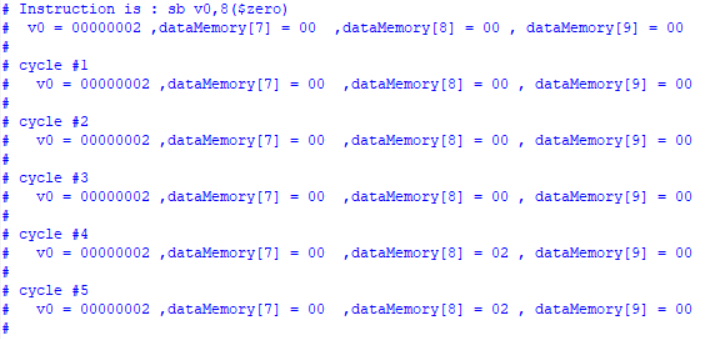
20.Multiplication



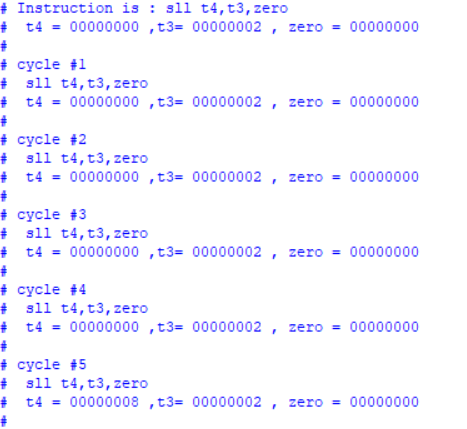
21.Negative Branch Taken



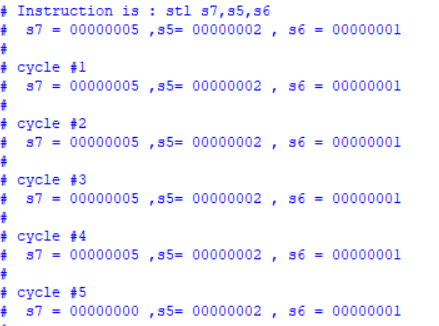
22.Store Byte



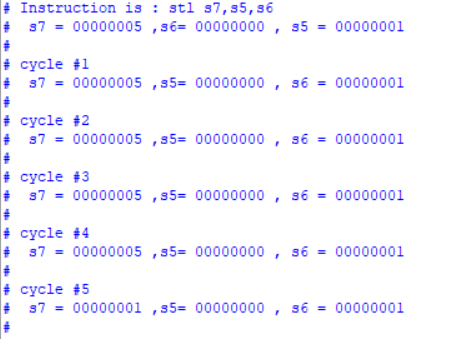
23.Shift Left



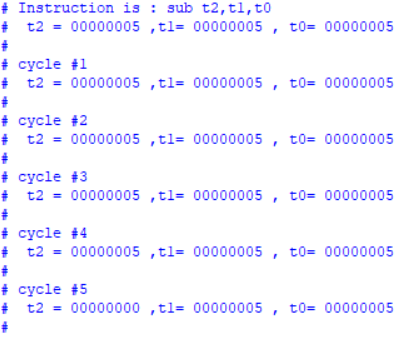
24.Set Less Than (Not Taken)



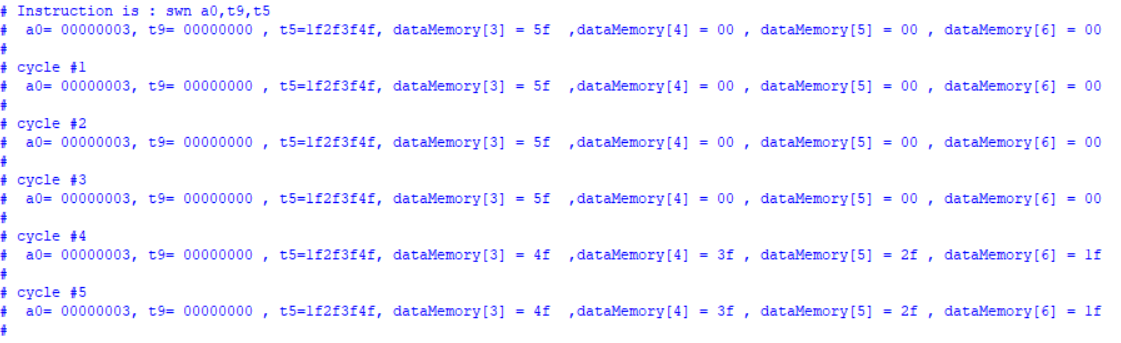
25.Set Less Than (Taken)



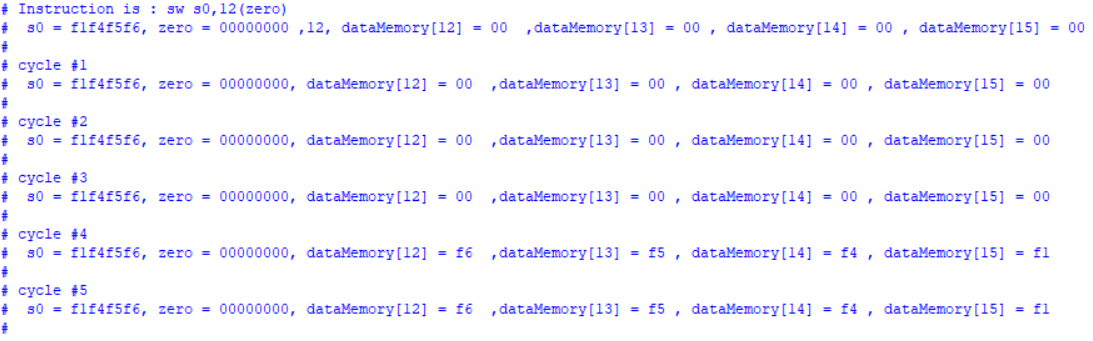
26.Subtraction



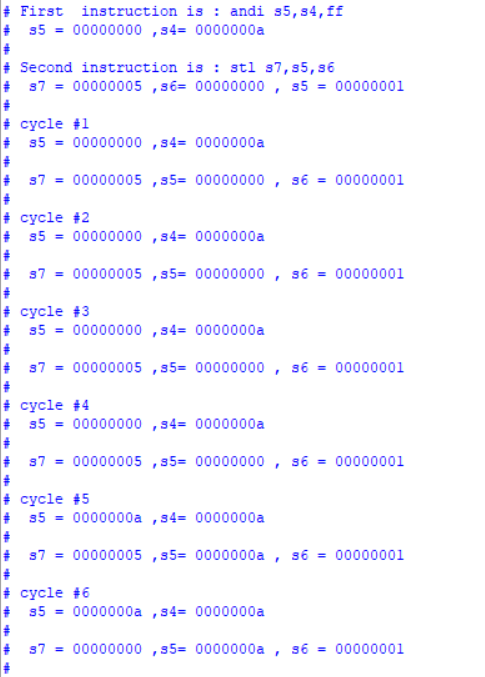
27.Store Word New



28.Store Word



29.Alu To Alu Forwarding



30.Load Word –Addition – Store Word (Dependency)  
  
 